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Sheet	1	of	2	Attorney Docket Number	LVPAT066US			

	U.S. PATENT DOCUMENTS									
Examiner Initials	Cite No.1	U.S. Patent (Ocument Kind Code ² (If known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relovant Passages or Relevant Figures Appear				
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ³ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁸ Applicant is to place a check mark here if English language Translation is ettached.

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			First Named Inventor	NADEAU-DOSTIE, Benoi			
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2			
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U. S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

OFFICE: U.S. DEPA Please type a plus sign (+) inside this box -> Complete if Known Substitute for form 1449B/PTO 10/690,594 **Application Number** October 23, 2003 INFORMATION DISCLOSURE Filing Date First Named Inventor Renoit NADEAU-DOSTIE STATEMENT BY APPLICANT **Group Art Unit Examiner Name** (use as many sheets as necessary) LVPAT066US of 2 Attorney Docket Number Sheet OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) atte of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-iss publisher, city and/or country where published. Examiner Cite Initiats' No. SCHANSTRA ET AL, "Semiconductor Manufacturing Process Monitoring Using Built-In Self-Test for Embedded Memories", Proceedings of the International Test Conference, New York, N NAGURA Y EX AL., Test Cost Reduction by At-Speed BISP for Embedded DRAMs", Proceedings of the International Test Conference 2001. J.C 2001. Baltimore, MD, Oct. 30 - Nov.1, 2001, New York, NY. Date Examiner Considered gnature EXAMINER: Initial if reference considered, whether or not cliation is in conformance with MPEP 609. Draw line through citation if not in conformation in confo ton bns sory Unique citation designation number. 2 Applicant is to place a check mark here if English language Translation is attached. Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any community on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U. S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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